

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

CIRCUIT AND METHOD OF ESTABLISHING DC BIAS LEVELS
IN AN RF POWER AMPLIFIER

Application Number : 10/743221



Confirmation Number: 7643

First Named Applicant: Alan Westwick

Attorney Docket Number: SIL.P0063

Art Unit: 2817

Examiner: Steven Mottola

Search string: (6727754 or 5726603 or 6630861 or 6118989 or 6756849 or 6828859 or 6894565
or 20040174218 or 20050068103).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<i>[initials]</i>	1	6727754	2004-04-27	Dupuis et al.		330	254
<i>[initials]</i>	2	5726603	1998-03-10	Chawla et al.		330	269
<i>[initials]</i>	3	6630861	2003-10-01	Kawaoka		330	133
<i>[initials]</i>	4	6118989	2000-09-01	Abe et al.		455	127
<i>[initials]</i>	5	6756849	2004-06-29	Dupuis et al.		330	229
<i>[initials]</i>	6	6828859	2004-12-07	Dupuis et al.		330	279
<i>[initials]</i>	7	6894565	2005-05-17	Bocock et al.		330	285

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
<i>[initials]</i>	1	20040174218	2004-09-09	Dupuis et al.		330	285
<i>[initials]</i>	2	20050068103	2005-03-31	Dupuis et al.		330	281

Signature

Examiner Name	Date
Steven J. Mottola	10-17-05